

PATENT NUMBER

U.S. UTILITY Patent Application.

O.I.P.E.

PATENT DATE

SCANNED

-0.4

APPLICATION NO. 09/715748	CONT/PRIOR F	CLASS 438	SUBCLASS 2.37	ART UNIT 2842 2.516	EXAMINER Barry
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APPLICANTS

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3.4.1

Method of forming a diode for integration with a semiconductor device and method of forming a transistor device having an integrated diode

PTO-2040
12/89

ISSUING CLASSIFICATION								
ORIGINAL			CROSS REFERENCE(S)					
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION								

Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) _____ (Date)		ISSUE FEE	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)		ISSUE BATCH NUMBER	

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